

115

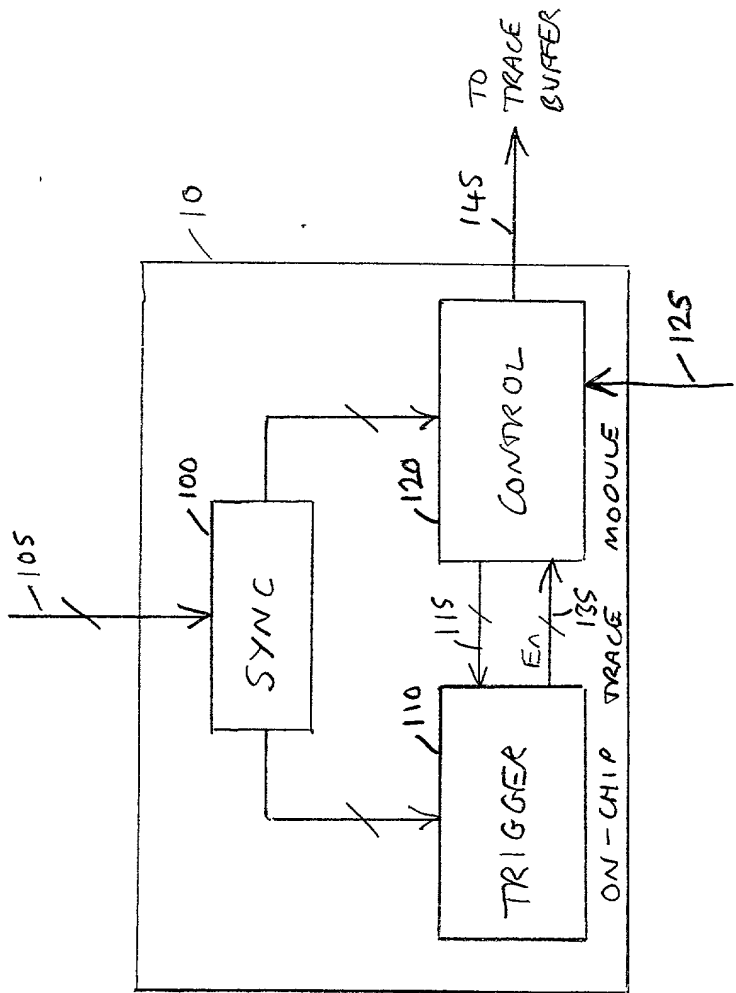
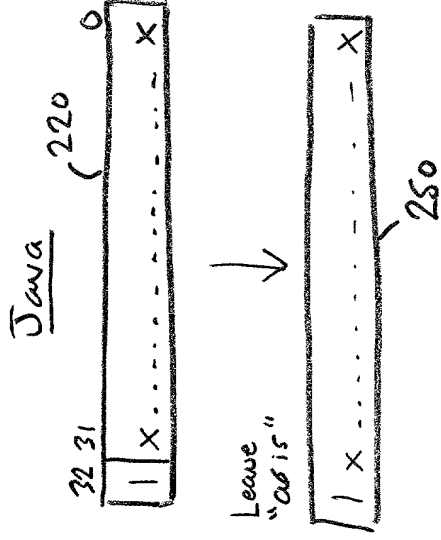
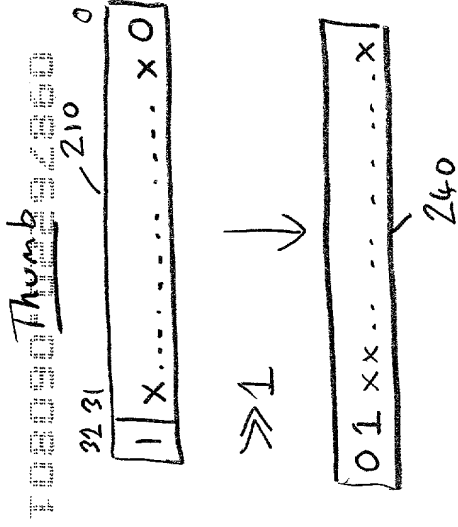
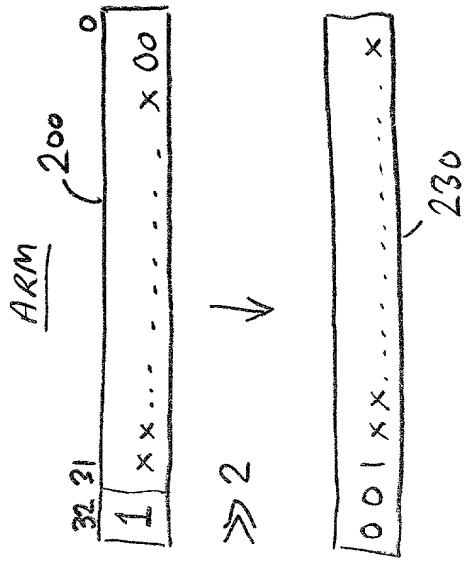


FIG. 2



Top 3 bits of 33 bit value

001 → ARM  
 01x → Thumb  
 1xx → Java  
 000 → Reversed

FIG. 3

# Encoded Instruction Address

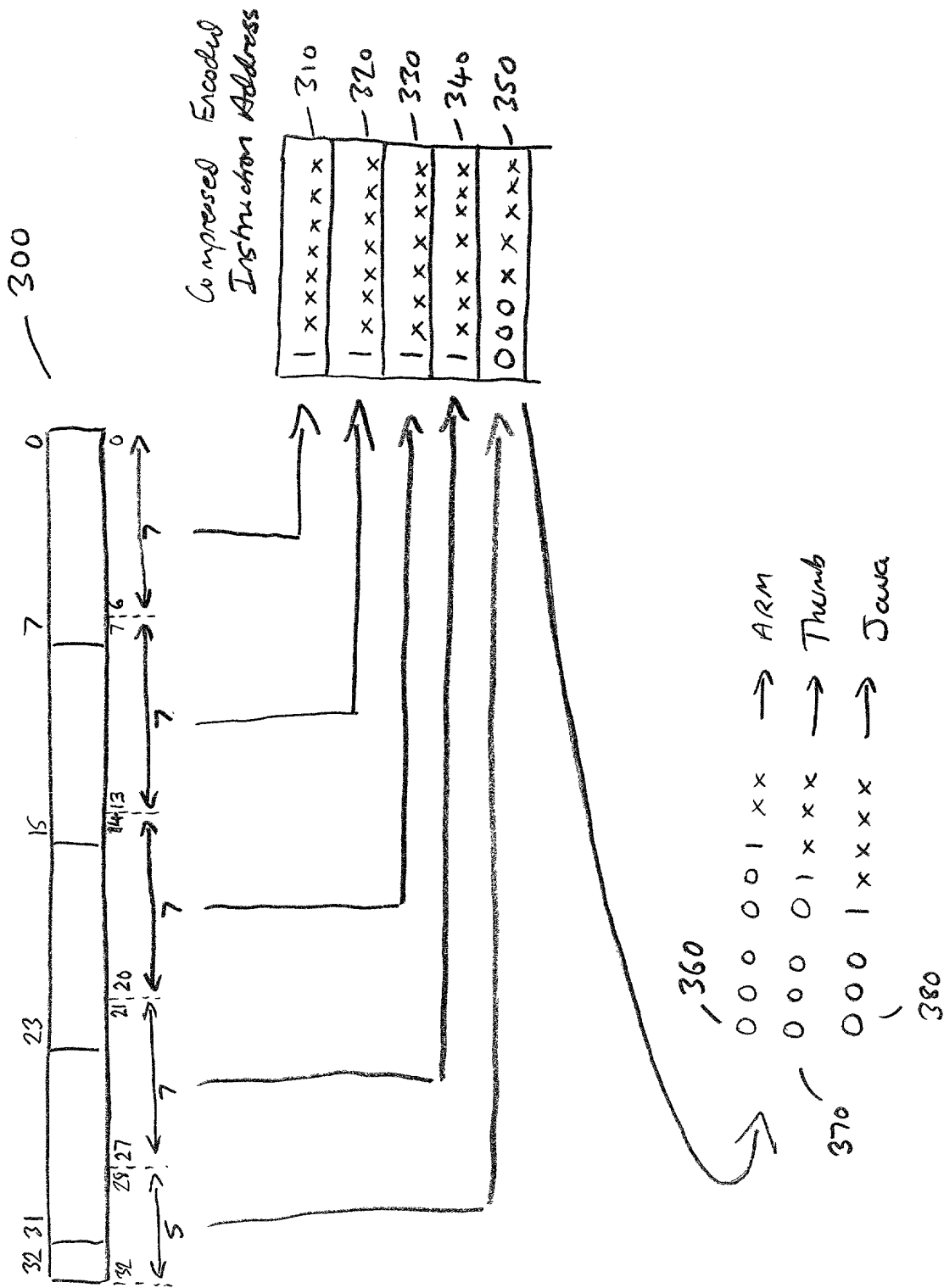


FIG. 4

(A)	00001000	400	10000000 10001000 10000000 10000000 00000100	430
(T)	00001100	410	10000000 10010001 10000000 10000000 00001000	440
(T)	00001200	420	10000000 00010010	450

FIG. 5

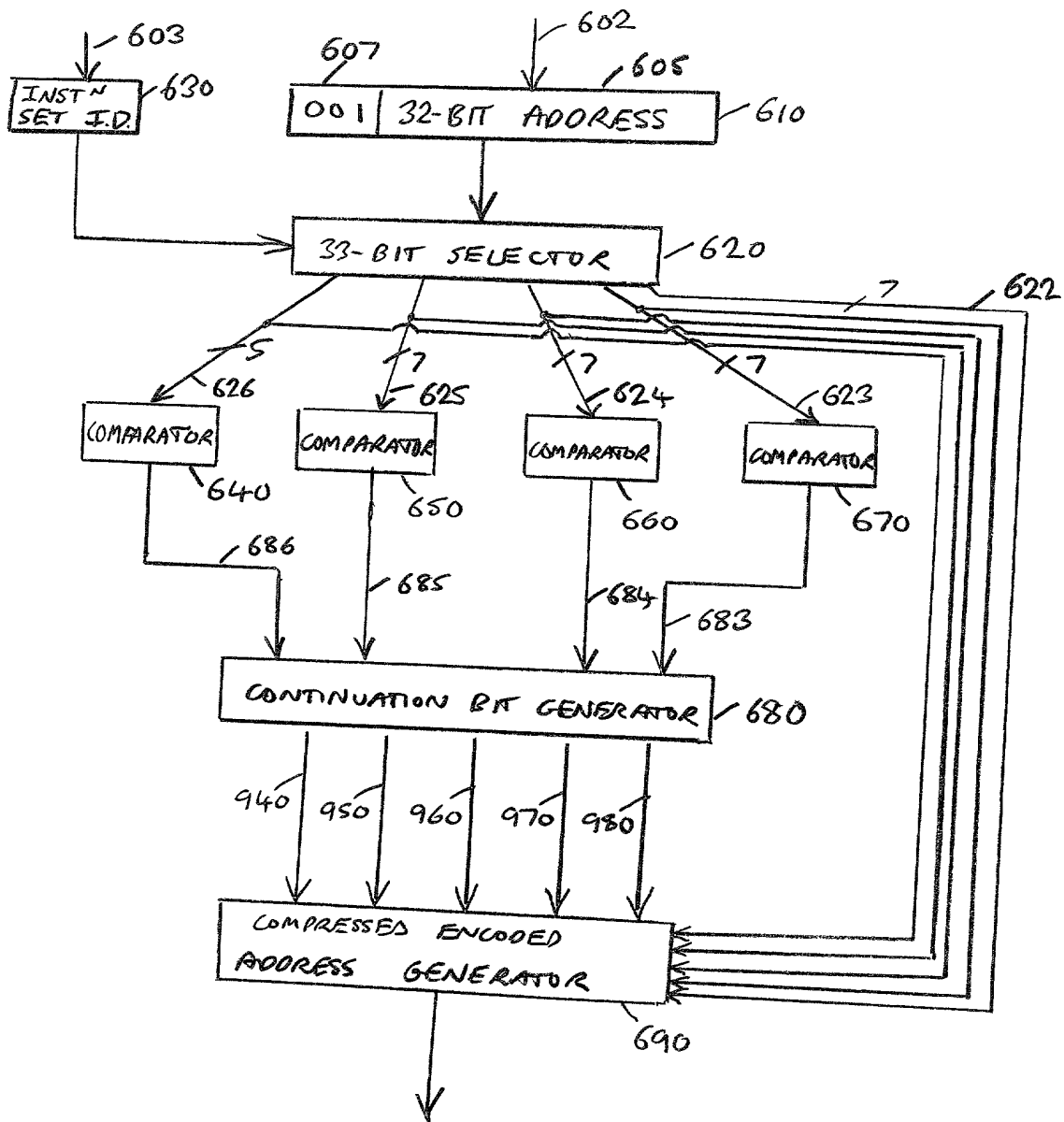


FIG. 6

# 33-BIT SELECTOR

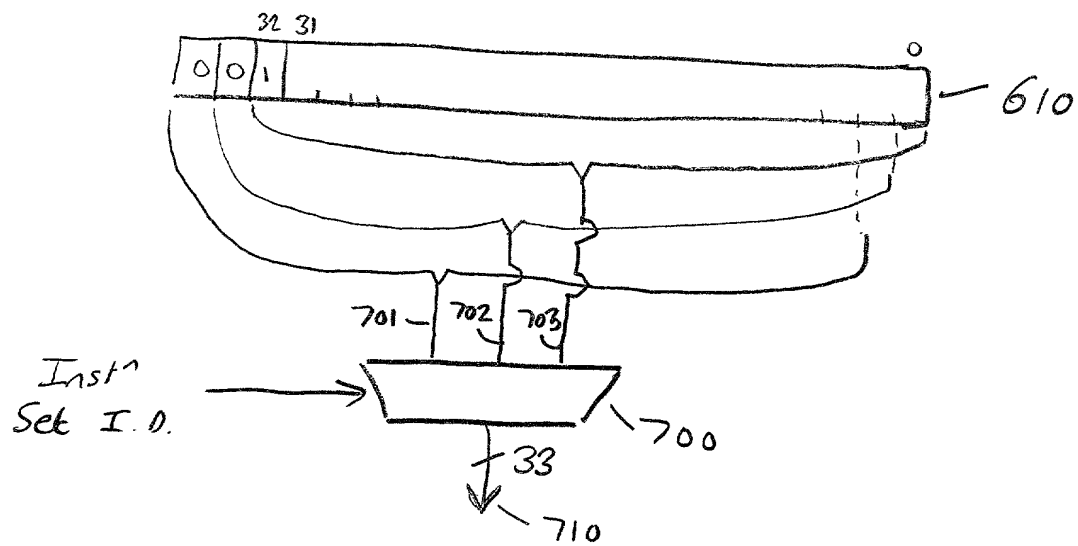
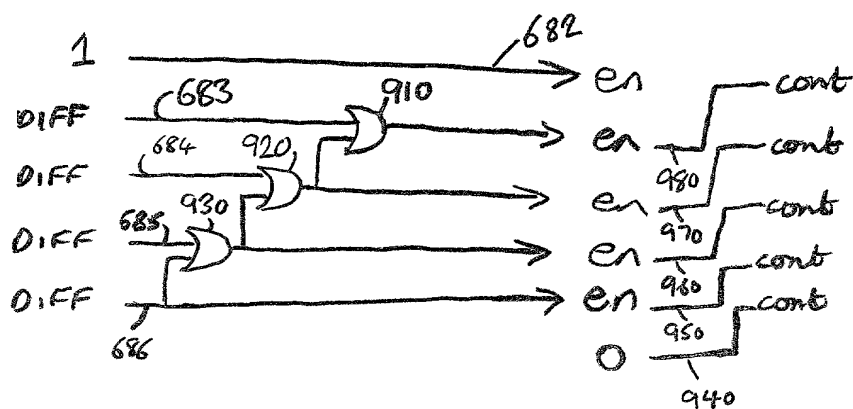


FIG. 7

[illegible]

FIG 9





# COMPRESSED ENCODED ADDRESS GENERATOR

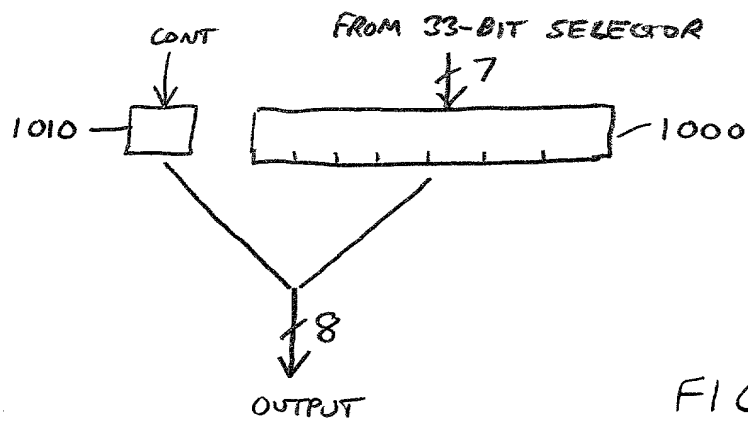


FIG 10 A

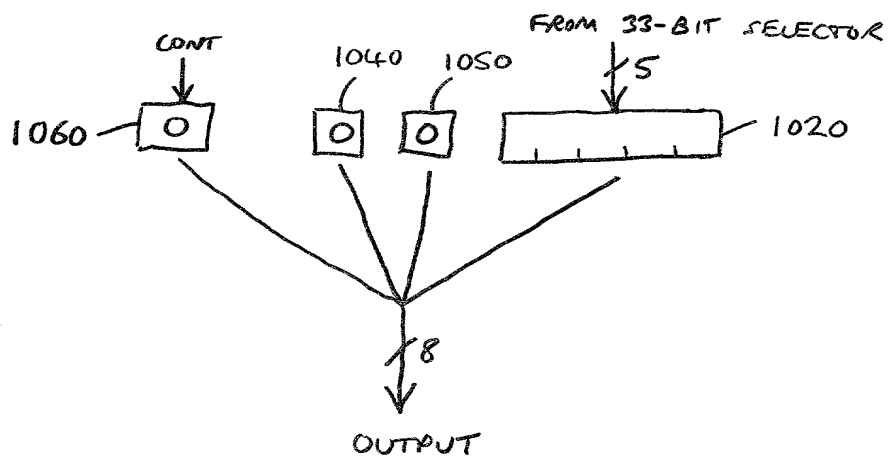


FIG. 10 B